

**SINGLE-STEP PROCESSING**

**ABSTRACT OF THE DISCLOSURE**

In one embodiment, techniques are disclosed for causing a programmable processor to process one instruction 5 at a time. Single-step debugging may be performed by taking an exception after each instruction or by invoking emulation mode after each instruction. The particular single-step debugging technique may be based upon state of control bits, or may be based upon the processor's current 10 mode of operation, or both.

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